

2643



Micronas.6581

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Andreas Menkhoff GROUP: 2643
SERIAL NO: 10/089,654 EXAMINER: Not yet assigned
FILED: April 1, 2002
FOR: CONTROL LOOP FOR DIGITAL SIGNALS

Assistant Commissioner of Patents
Washington, D.C. 20231

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Sir:

INFORMATION DISCLOSURE STATEMENT

In compliance with 37 C.F.R. §§1.56, 1.97, and 1.98, Applicant submits copies of the documents listed on the attached Form PTO-1449.

The Commissioner is authorized to charge Deposit Order Account No. 19-0079 for any further fee that is required.

Respectfully submitted,

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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited on the date shown below in an envelope with sufficient postage for first class mail addressed to the: Commissioner of Patents and Trademarks, Washington, D.C. 20231.

Tanya Bellanti
Tanya Bellanti
1/10/03
Date

FORM PTO-1449 SAMUELS, GAUTHIER & STEVENS LLP
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ATTORNEY DOCKET NO. 6581

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FILING DATE: 4/1/02

EXAMINER: Unknown

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
9	AH						

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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
	AI	JP 2 285804	11/26/90	JP			Yes-abstract
	AJ						
	AK						
	AL						
	AM						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL		
	AN	Tietze et al. "Semiconductor Circuit Engineering", Springer-Verlag 1980; English translation of pages 484-487.
	AO	
	AP	

EXAMINER

DATE CONSIDERED

EXAMINER:

Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.